

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **A. TROYA et al.**

Docket No.: **536-009.014**

Application No.: **10/521,396**

Group No.: **To Be Assigned**

Filed: **January 14, 2005**

Examiner: **To Be Assigned**

For: **Method and device for frame detection and synchronizer**

Commissioner for Patents
U.S. Patent & Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Applicant submits herewith references of which they are aware, which they believe may be material to the examination of this application and in respect of which they may have a duty to disclose in accordance with 37 CFR 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 CFR 1.56, it is not intended to constitute an admission that any document referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined under 37 CFR 1.56(a) exists.

I hereby certify that this correspondence is being deposited with the United States Postal Service on this date, March 15, 2005, with sufficient postage as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Annemarie Maher

Attorney Docket No. 536-009.014
Serial No. 10/521,396

Enclosed is an International Search Report dated February 27, 2004, issued in International Patent Application No. PCT/EP03/07750 (filed on July 16, 2003), from which the present application has entered the US national stage. The relevance of each reference is specifically explained in either the Search Report or in the application specification.

Also enclosed is a Form PTO-1449 listing the cited references. Copies of the cited references are also enclosed.

The information disclosure statement submitted herewith is being filed within three months of the filing date of the application or date of entry into the national stage of an international application or before the mailing of a first Office action on the merits, whichever occurs last and so the undersigned respectfully submits that no fee is due for filing this IDS. Should any fees be due of which the undersigned is unaware, the Commissioner is hereby authorized to charge deposit account 23-0442 any fee deficiency required to submit this IDS.

A PTO-1449 with cited references is enclosed.

Respectfully submitted,

Date: March 15, 2005

Reg. No.: 41,266

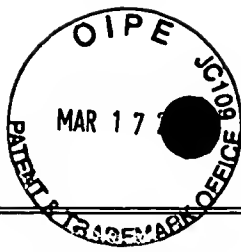
Telephone No.: (203) 261-1234

Customer No.: 004955



James A. Retter

Ware, Fressola, Van Der Sluys &
Adolphson LLP
Bradford Green, Building Five
755 Main Street, P.O. Box 224
Monroe, CT 06468



FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT				ATTY DOCKET NO. 536-009.014		SERIAL NO. 10/521,396	
				APPLICANT: A. TROYA et al.			
				FILING DATE: January 14, 2005		ART UNIT: To be assigned	
UNITED STATES PATENT DOCUMENTS							
EXAM. INITIAL		DOCUMENT NUMBER	ISSUE/PUBL DATE	INVENTOR/ASSIGNEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	INVENTOR/ASSIGNEE	CLASS	SUBCLASS	TRANSLATION YES/NO
/LF/		WO 98/10545	03/12/1998	M. Isaksson et al. - PCT			
/LF/		EP 1 049 302	11/02/2000	R. Bohnke et al. - EP			
OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)							
/LF/	1	IEEE P802.11a/D7.0, Supplement to IEEE Standard 802.11, "DRAFT Supplement to STANDARD for Information Technology - Telecommunications and information exchange between systems - Local and metropolitan area networks - Specific Requirements - Part 11: Wireless LAN Medium Access Control (MAC) and Physical Layer (PHY) Specifications: High Speed Physical Layer in the 5 GHz Band," 1999.					
/LF/	2	Y. Chiu et al., "OFDM Receiver Design," XP-002263353, Final Report December 12, 2000.					
/LF/	3	E. Grass et al., "On the single-chip implementation of a Hiperlan/2 and IEEE 802.11a capable modem," XP-001076795, <i>IEEE Personal Communications</i> , December 2001, pp. 48-57.					
/LF/	4	M. Krstic et al., "Optimized low-power synchronizer design for the IEEE 802.11a standard," <i>ICASSP</i> 2003, pp. 333-336.					
/LF/	5	A. Troya et al., "OFDM synchronizer implementation for an IEEE 802.11a compliant modem," XP-009022041, <i>Proceedings of the IASTED International Conference</i> , July 17-19, 2002, pp. 152-157.					
/LF/	6	L. Schwoerer et al., "VLSI implementation of IEEE 802.11a physical layer," <i>6th International OFDM Workshop</i> , Hamburg, 2001, pp. 28-1 - 28-4.					
Examiner /Leon Flores/				Date: 08/27/2009			